

## WEST Search History





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	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L60	6256726.pn. or 5233694.pn.	3
<input type="checkbox"/>	L59	4747045.pn. or 4615004.pn.	4
<input type="checkbox"/>	L58	=1988	170
<input type="checkbox"/>	L54	L53 and l6	727
<input type="checkbox"/>	L53	L50 and (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	1066
<input type="checkbox"/>	L52	L50 and l6(ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	466426
<input type="checkbox"/>	L51	L50 and l6	947
<input type="checkbox"/>	L50	(fetch\$5 or prefetch\$5) near3 instructions near3 (parallel\$3 or simultaneous\$4 or cocurrent\$3 or (("same" or one or single) near4 (clock or cycle or period)))	1633
<input type="checkbox"/>	L49	(fetch\$5 or prefetch\$5) near4 instructions near4 (parallel\$3 or simultaneous\$4 or cocurrent\$3 or (("same" or one or single) near4 (clock or cycle or period)))	1949
<input type="checkbox"/>	L48	(fetch\$5 or prefetch\$5) near5 instructions near5 (parallel\$3 or simultaneous\$4 or cocurrent\$3 or (("same" or one or single) near4 (clock or cycle or period)))	2218
<input type="checkbox"/>	L47	L46 not l5	41
<input type="checkbox"/>	L46	L42 near50 (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	125
<input type="checkbox"/>	L45	L43 and l7	29
<input type="checkbox"/>	L44	L43 and l6	971
<input type="checkbox"/>	L43	L42 and (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	1449
<input type="checkbox"/>	L42	(fetch\$5 or prefetch\$5) near5 instruction\$1 near5 (parallel\$3 or simultaneous\$4 or cocurrent\$3 or (("same" or one or single) near4 (clock or cycle or period)))	2218
<input type="checkbox"/>	L41	l36 and l6	2188
<input type="checkbox"/>	L40	L39 not l37	2
<input type="checkbox"/>	L39	l35 near75 (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	276
<input type="checkbox"/>	L38	L37 not l34	6
<input type="checkbox"/>	L37	l35 near50 (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	274
<input type="checkbox"/>	L36	l35 and (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	3708
<input type="checkbox"/>	L35	(fetch\$5 or prefetch\$5) near8 instruction\$1 near8 (parallel\$3 or simultaneous\$4 or cocurrent\$3 or one or single)	6254
<input type="checkbox"/>	L34	L33 near35 (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	268
<input type="checkbox"/>	L33	(fetch\$5 or prefetch\$5) near8 instruction near8 (parallel\$3 or simultaneous\$4 or cocurrent\$3 or one or single)	6249

<input type="checkbox"/>	L32	L31 not l5	120
<input type="checkbox"/>	L31	L30 near35 (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	268
<input type="checkbox"/>	L30	(fetch\$5 or prefetch\$5) near8 instructions near8 (parallel\$3 or simultaneous\$4 or cocurrent\$3 or one or single)	6249
<input type="checkbox"/>	L29	L27 and l7	8
<input type="checkbox"/>	L28	L27 and l6	109
<input type="checkbox"/>	L27	L4 near35 (ALU or arithmetic\$3 or adder\$1 or multiplier\$1)	197
<input type="checkbox"/>	L26	L25 not l5	10
<input type="checkbox"/>	L25	L4 near35 (ALU or arithmetic\$3)	158
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L24	l5 not l10	41
<input type="checkbox"/>	L23	l16 and l19	1
<input type="checkbox"/>	L22	l16 and l18	0
<input type="checkbox"/>	L21	l16 and l7	11
<input type="checkbox"/>	L20	l16 and l6	109
<input type="checkbox"/>	L19	(708/506,632)[CCLS]	35
<input type="checkbox"/>	L18	(710/305 )![CCLS]	1527
<input type="checkbox"/>	L17	(708/506, 632)![CCLS]	5
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L16	L12 and switch\$6 and ("same" or one or single) near5 (clock or period or stage or cycle)	166
<input type="checkbox"/>	L15	L13 and l7	12
<input type="checkbox"/>	L14	L13 and l6	111
<input type="checkbox"/>	L13	L12 and switch\$6	169
<input type="checkbox"/>	L12	L11 and l4	268
<input type="checkbox"/>	L11	(bypass\$5 or alternat\$6) near7 (arithmetic\$3 or alu or adder\$ or multiplier\$1)	8139
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L10	l6 and l5	72
<input type="checkbox"/>	L9	L7 and l5	8
<input type="checkbox"/>	L8	L7 and l6	539
<input type="checkbox"/>	L7	(712/221,222)[CCLS]	539
<input type="checkbox"/>	L6	(712/2-300)[CCLS]	11268
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L5	L4 near15 (ALU or arithmetic\$3)	148
<input type="checkbox"/>	L4	(fetch\$5 or prefetch\$5) near5 instructions near5 (parallel\$3 or simultaneous\$4 or cocurrent\$3 or one or single)	4889
	<i>DB=USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L3	L2 and (fetch\$5 or prefetch\$5)	0
<input type="checkbox"/>	L2	L1 and (parallel\$5 or concurent\$4 or simultaneous\$4)	1

☐ L1 4766564.pn.

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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

**1. A seventh-generation x86 microprocessor**Golden, M.; Hesley, S.; Scherer, A.; Crowley, M.; Johnson, S.C.; Meier, S.; Meyer, D.; Moench, J.C.  
Partovi, H.; Weber, F.; White, S.; Wood, T.; Yong, J.;

Solid-State Circuits, IEEE Journal of

Volume 34, Issue 11, Nov. 1999 Page(s):1466 - 1477

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